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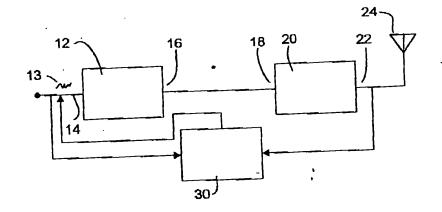


FIGURE 1

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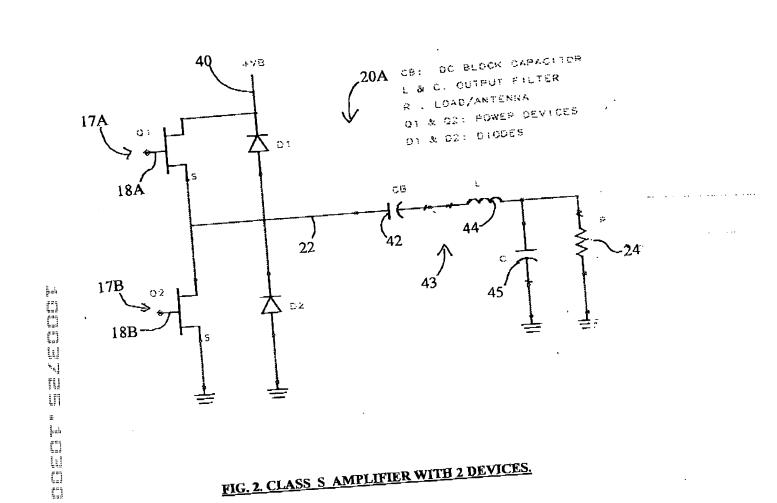


FIG. 2. CLASS S AMPLIFIER WITH 2 DEVICES.

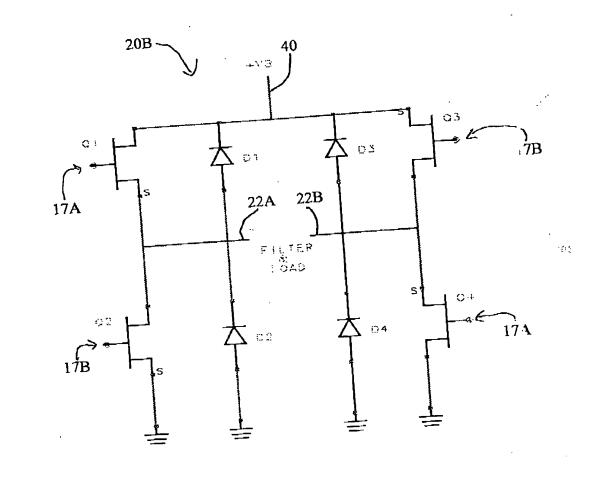


FIG. 3. CLASS S AMPLIFIER WITH 4 DEVICES. (H. CONFIGURATION.)

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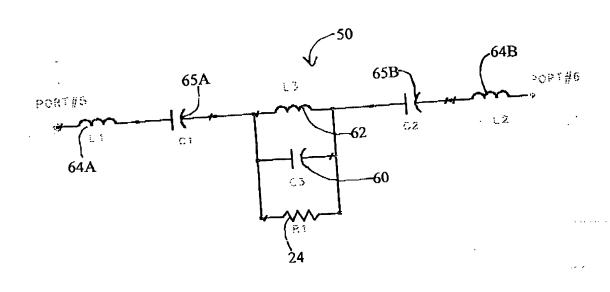


FIG. 4. CLASS SAMPLIFIER: OUTPUT FILTER AND LOAD SCHEMA FIC

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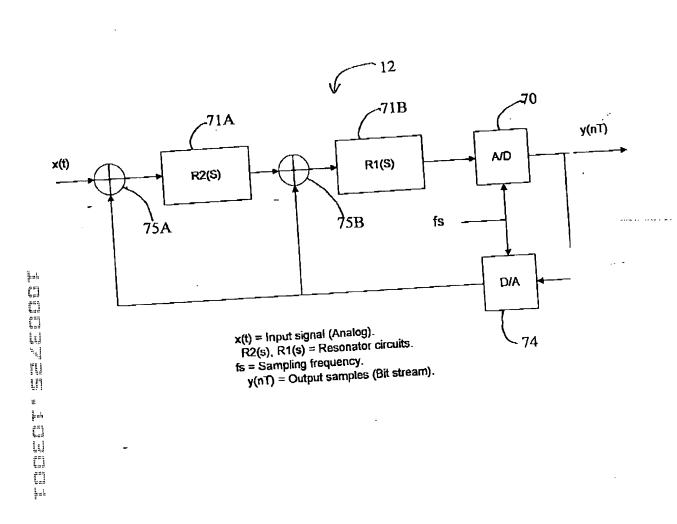
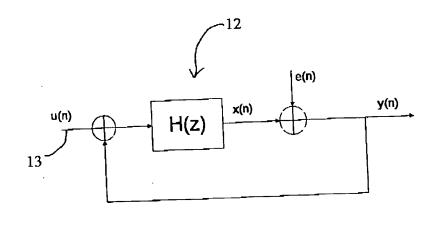


FIG. 5. CONTINUOUS TIME BANDPASS DELTA SIGMA MODULATOR USING DOUBLE FEEDBACK LOOP.

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u(n)= Input signal x(n)= Input to Quantizer c(n)= Quantization noise as additive noise non-correlated with input y(n)= Output bit stream

FIG. 6. SIMPLIFIED DISCRETE EQUIVALENT OF DELTA SIGMA MODULATOF.

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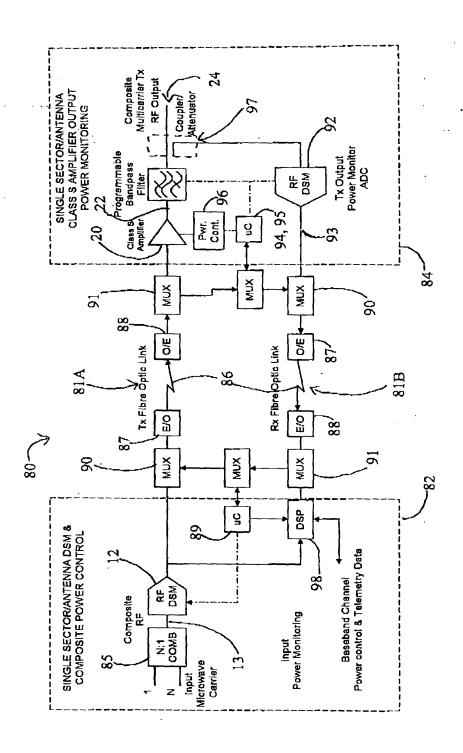


FIGURE 7A

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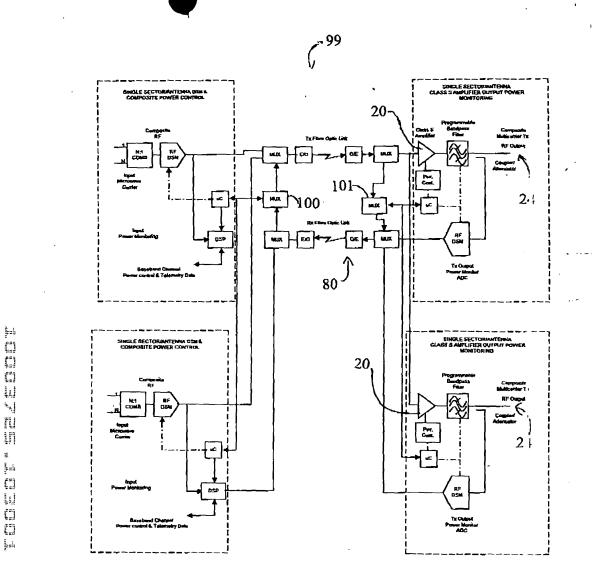


FIGURE 7B